

MPW#	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW1426	12nm	10/21/2020	10/28/2020	11/19/2020	12/2/2020	12/9/2020	12/16/2020	Mid Mar 2021-End Mar 2021	Early Apr 2021-Mid Apr 2021	12LP
MPW4R07	45RFSOI_45nm	10/13/2020		11/10/2020	11/23/2020	11/23/2020	12/7/2020	Mid Apr 2021-End Apr 2021	Mid Apr 2021-End Apr 2021	45RF
MPW8H07	8HP_0.13um	10/13/2020		11/10/2020	11/23/2020	11/23/2020	12/7/2020	End Apr 2021-Early May 2021	End Apr 2021-Early May 2021	8HP_0.13um
MPW0699	55nm	10/12/2020	10/19/2020	11/10/2020	11/23/2020	11/23/2020	12/7/2020	Mid Feb 2021-Early Mar 2021	Mid Apr 2021-Mid Apr 2021	55LPX/55LPe/55ULP/55BCDL/55HV
MPW18G2	0.18um	10/12/2020	10/19/2020	11/9/2020	11/23/2020	11/23/2020	12/7/2020	Early Feb 2021-End Feb 2021	Mid Mar 2021-Mid Mar 2021	Fab35_IC/BCDlite/UHV/MCU
MPW2237	22nm	9/15/2020	9/22/2020	10/13/2020	10/27/2020	10/27/2020	11/9/2020	Early Jan 2021-Mid Jan 2021	End Jan 2021-Early Feb 2021	22FDX/22eMRAM. Projected ship dates are for NON-22eMRAM.
MPW8S01	8SW_0.13um	9/15/2020	9/22/2020	10/13/2020	10/27/2020	10/27/2020	11/9/2020	Early Mar 2021-Early Mar 2021	Early Mar 2021-Early Mar 2021	8SW
MPW9W05	9WG_90nm	9/8/2020		10/7/2020	10/20/2020	10/20/2020	11/3/2020	Mid Mar 2021-End Mar 2021	Mid Mar 2021-End Mar 2021	9WG
MPW0465	40nm	9/8/2020	9/15/2020	10/6/2020	10/20/2020	10/20/2020	11/2/2020	End Jan 2021-End Jan 2021	End Mar 2021-End Mar 2021	40LP/40HV/40NVM
MPW13B3	0.11/0.13um	9/7/2020	9/14/2020	10/6/2020	10/19/2020	10/19/2020	11/2/2020	Early Jan 2021-Early Jan	Early Mar 2021-Early Mar	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



2021 2021

*: Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff, CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW #	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Batch Bare Die	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW9H08	9HP_90nm	8/31/2020		9/29/2020	10/12/2020	10/12/2020	10/26/2020	Early Mar 2021-Mid Mar 2021	Early Mar 2021-Mid Mar 2021	9НР
MPW1509	0.15um	8/31/2020	9/7/2020	9/29/2020	10/12/2020	10/12/2020	10/26/2020	End Dec 2020-End Dec 2020	End Jan 2021-End Jan 2021	F35_MCU
MPW8X03	8XP_0.13um	8/24/2020		9/22/2020	10/5/2020	10/5/2020	10/19/2020	Early Mar 2021-Mid Mar 2021	Early Mar 2021-Mid Mar 2021	8XP
MPW0698	55nm	8/24/2020	8/31/2020	9/22/2020	10/5/2020	10/5/2020	10/19/2020	End Dec 2020-Early Jan 2021	Early Mar 2021-Early Mar 2021	55LPX/55LPe/55ULP/55BCDL/55NVM
MPW0361	28nm	8/10/2020	8/17/2020	9/8/2020	9/21/2020	9/21/2020	10/5/2020	Mid Dec 2020-End Dec 2020	End Dec 2020-Mid Jan 2021	28SLP/28SLPe/28SLP-RF/28SLP-HV
MPW8H06	8HP_0.13um	8/10/2020		9/8/2020	9/21/2020	9/21/2020	10/5/2020	Mid Feb 2021-Early Mar 2021	Mid Feb 2021-End Feb 2021	8HP_0.13um
MPW18G1	0.18um	8/3/2020	8/10/2020	9/1/2020	9/14/2020	9/14/2020	9/28/2020	End Nov 2020-Mid Dec 2020	Early Jan 2021-Early Jan 2021	Fab35_IC/BCDlite/UHV/MCU
MPW4R06	45RFSOI_45nm	7/27/2020		8/25/2020	9/7/2020	9/7/2020	9/21/2020	Early Feb 2021-Mid Feb 2021	Early Feb 2021-Mid Feb 2021	45RF
MPW13B2	0.11/0.13um	7/27/2020	8/3/2020	8/25/2020	9/7/2020	9/7/2020	9/21/2020	End Nov 2020-End Nov 2020	End Jan 2021-End Jan 2021	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD
MPW1425	12nm	7/22/2020	7/29/2020	8/20/2020	9/2/2020	9/9/2020	9/16/2020	Mid Dec 2020-End Dec 2020	Early Jan 2021-Mid Jan 2021	12LP

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW#	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW2236	22nm	7/20/2020	7/27/2020	8/18/2020	8/31/2020	8/31/2020	9/14/2020	Early Nov 2020-Mid Nov 2020	End Nov 2020-Early Dec 2020	22FDX
MPW0697	65nm	7/13/2020	7/20/2020	8/11/2020	8/24/2020	8/24/2020	9/7/2020	Early Nov 2020-Mid Nov 2020	End Dec 2020-End Dec 2020	65G/65LPE
MPW8H05	8HP_0.13um	6/29/2020		7/28/2020	8/10/2020	8/10/2020	8/24/2020	Early Jan 2021-Mid Jan 2021	Early Jan 2021-Mid Jan 2021	8HP_0.13um
MPW0696	55nm	6/29/2020	7/6/2020	7/28/2020	8/10/2020	8/10/2020	8/24/2020	Early Nov 2020-Early Nov 2020	Early Jan 2021-Early Jan 2021	55LPX/55LPe/55ULP/55BCDL/55HV
MPW0464	40nm	6/22/2020	6/29/2020	7/21/2020	8/3/2020	8/3/2020	8/17/2020	Early Nov 2020-Early Nov 2020	Early Jan 2021-Early Jan 2021	40LP/40NVM
MPW9H07	9HP_90nm	6/15/2020		7/14/2020	7/27/2020	7/27/2020	8/10/2020	End Dec 2020-Early Jan 2021	End Dec 2020-Early Jan 2021	9HP
MPW9W04	9WG_90nm	6/8/2020		7/7/2020	7/20/2020	7/20/2020	8/3/2020	Mid Dec 2020-End Dec 2020	Mid Dec 2020-End Dec 2020	9WG
MPW8X02	8XP_0.13um	6/1/2020		6/30/2020	7/13/2020	7/13/2020	7/27/2020	Early Dec 2020-Mid Dec 2020	Early Dec 2020-Mid Dec 2020	8XP
MPW2235	22nm	5/25/2020	6/1/2020	6/23/2020	7/6/2020	7/6/2020	7/20/2020	Mid Sept 2020-End Sept 2020	End Sept 2020-Early Oct 2020	22FDX/22eMRAM. Projected ship dates are for NON-22eMRAM.
MPW0360	28nm	5/18/2020	5/25/2020	6/15/2020	6/29/2020	6/29/2020	7/13/2020	Mid Sept 2020-End Sept 2020	Early Oct 2020-Mid Oct 2020	28SLP/28SLPe/28SLP-RF/28SLP-HV

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW #	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]		Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW13B1	0.11/0.13um	5/11/2020	5/18/2020	6/8/2020	6/22/2020	6/22/2020	7/6/2020	Early Sept 2020-Early Sept 2020	Early Nov 2020-Early Nov 2020	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD
MPW7W01	7WL_0.18um	4/27/2020		5/26/2020	6/8/2020	6/8/2020	6/22/2020	Early Nov 2020-Mid Nov 2020	Early Nov 2020-Mid Nov 2020	7WL_0.18um
MPW0695	55nm	4/27/2020	5/4/2020	5/26/2020	6/8/2020	6/8/2020	6/22/2020	Early Sept 2020-Early Sept 2020	Early Nov 2020-Early Nov 2020	55LPX/55LPe/55ULP/55BCDL/55NVM
MPW8H04	8HP_0.13um	4/27/2020		5/26/2020	6/8/2020	6/8/2020	6/22/2020	Early Nov 2020-Mid Nov 2020	Early Nov 2020-Mid Nov 2020	8HP_0.13um
MPW1424	12nm	4/22/2020	4/29/2020	5/21/2020	6/3/2020	6/10/2020	6/17/2020	Mid Sept 2020-End Sept 2020	Early Oct 2020-Mid Oct 2020	12LP
MPW8S00	8SW_0.13um	4/20/2020	4/27/2020	5/19/2020	6/1/2020	6/1/2020	6/15/2020	Early Oct 2020-Early Oct 2020	Early Oct 2020-Early Oct 2020	8SW
MPW18G0	0.18um	4/20/2020	4/27/2020	5/19/2020	6/1/2020	6/1/2020	6/15/2020	Mid Aug 2020-End Aug 2020	End Sept 2020-End Sept 2020	Fab35_IC/BCDlite/UHV/MCU
MPW1508	0.15um	4/13/2020	4/20/2020	5/12/2020	5/25/2020	5/25/2020	6/8/2020	Early Aug 2020-Early Aug 2020	Early Sept 2020-Early Sept 2020	F35_MCU
MPW4R05	45RFSOI_45nm	4/6/2020		5/5/2020	5/18/2020	5/18/2020	6/1/2020	Mid Oct 2020-End Oct 2020	Mid Oct 2020-End Oct 2020	45RF
MPW9H06	9HP_90nm	3/16/2020		4/14/2020	4/27/2020	4/27/2020	5/11/2020	End Sept 2020-Early Oct 2020	End Sept 2020-Early Oct 2020	9НР

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW #	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW0463	40nm	3/16/2020	3/23/2020	4/14/2020	4/27/2020	4/27/2020	5/11/2020	End Jul 2020-Early Aug 2020	End Sept 2020-End Sept 2020	40LP/40HV/40NVM
MPW13B0	0.11/0.13um	3/16/2020	3/23/2020	4/14/2020	4/27/2020	4/27/2020	5/11/2020	Early Jul 2020-Mid Jul 2020	Early Sept 2020-Mid Sept 2020	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD
MPW2234	22nm	3/16/2020	3/23/2020	4/14/2020	4/27/2020	4/27/2020	5/11/2020	Early Jul 2020-Mid Jul 2020	End Jul 2020-Early Aug 2020	22FDX
MPW9W03	9WG_90nm	3/9/2020		4/7/2020	4/20/2020	4/20/2020	5/4/2020	Mid Sept 2020-End Sept 2020	Mid Sept 2020-End Sept 2020	9WG
MPW8X01	8XP_0.13um	3/2/2020		3/31/2020	4/13/2020	4/13/2020	4/27/2020	Early Sept 2020-Mid Sept 2020	Early Sept 2020-Mid Sept 2020	8XP
MPW0694	55nm	2/24/2020	3/2/2020	3/24/2020	4/6/2020	4/6/2020	4/20/2020	End Jun 2020-Early Jul 2020	End Aug 2020-End Aug 2020	55LPX/55LPe/55ULP/55BCDL/55HV
MPW0359	28nm	2/17/2020	2/24/2020	3/17/2020	3/30/2020	3/30/2020	4/13/2020	Mid Jun 2020-End Jun 2020	Early Jul 2020-Mid Jul 2020	28SLP/28SLPe/28SLP-RF/28SLP-HV
MPW8H03	8HP_0.13um	2/10/2020		3/10/2020	3/23/2020	3/23/2020	4/6/2020	Mid Aug 2020-End Aug 2020	Mid Aug 2020-End Aug 2020	8HP_0.13um
MPW0693	65nm	1/27/2020	2/3/2020	2/25/2020	3/9/2020	3/9/2020	3/23/2020	End May 2020-End May 2020	Mid Jul 2020-Mid Jul 2020	65G/65LPE
MPW18F9	0.18um	1/27/2020	2/3/2020	2/25/2020	3/9/2020	3/9/2020	3/23/2020	End May 2020-Early Jun 2020	Early Jul 2020-Early Jul 2020	Fab35_IC/BCDlite/UHV/MCU

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW#	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW1423	12nm	1/22/2020	1/29/2020	2/20/2020	3/4/2020	3/11/2020	3/18/2020	Mid Jun 2020-End Jun 2020	Early Aug 2020-Mid Aug 2020	12LP
MPW8W00	8WL_0.13um	1/20/2020		2/18/2020	3/2/2020	3/2/2020	3/16/2020	End Jul 2020-Early Aug 2020	End Jul 2020-Early Aug 2020	8WL_0.13um
MPW9W02	9WG_90nm	1/17/2020		1/18/2020	1/20/2020	1/20/2020	2/3/2020	Mid Jun 2020-End Jun 2020	Mid Jun 2020-End Jun 2020	9WG
MPW9H04	9HP_90nm	1/17/2020		10/29/2019	11/11/2019	11/11/2019	11/25/2019	Mid Apr 2020-End Apr 2020	Mid Apr 2020-End Apr 2020	9НР
MPW4R04	45RFSOI_45nm	1/16/2020		2/4/2020	2/17/2020	2/17/2020	3/2/2020	Mid Jul 2020-End Jul 2020	Mid Jul 2020-End Jul 2020	45RF
MPW13A9	0.11/0.13um	1/13/2020	1/20/2020	2/10/2020	2/24/2020	2/24/2020	3/9/2020	Early May 2020-Mid May 2020	Early Jul 2020-Early Jul 2020	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD
MPW2233	22nm	1/6/2020	1/13/2020	2/4/2020	2/17/2020	2/17/2020	3/2/2020	End Apr 2020-Early May 2020	Mid May 2020-End May 2020	22FDX/22eMRAM. Projected ship dates are for NON-22eMRAM.
MPW8H02	8HP_0.13um	12/30/2019		1/28/2020	2/10/2020	2/10/2020	2/24/2020	Mid Jul 2020-End Jul 2020	Mid Jul 2020-End Jul 2020	8HP_0.13um
MPW1507	0.15um	12/23/2019	12/30/2019	1/21/2020	2/3/2020	2/3/2020	2/17/2020	Mid Apr 2020-Mid Apr 2020	Mid May 2020-Mid May 2020	F35_MCU
MPW0692	55nm	12/23/2019	12/30/2019	1/21/2020	2/3/2020	2/3/2020	2/17/2020	End Apr 2020-Early May 2020	End Jun 2020-End Jun 2020	55LPX/55LPe/55ULP/55BCDL/55NVM

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW #	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW9H05	9HP_90nm	12/16/2019		1/14/2020	1/27/2020	1/27/2020	2/10/2020	End Jun 2020-Early Jul 2020	End Jun 2020-Early Jul 2020	9НР
MPW0462	40nm	12/16/2019	12/23/2019	1/14/2020	1/27/2020	1/27/2020	2/10/2020	End Apr 2020-Early May 2020	End Jun 2020-End Jun 2020	40LP/40NVM
MPW8X00	8XP_0.13um	12/13/2019		12/24/2019	1/6/2020	1/6/2020	1/20/2020	End Jun 2020-Early Jul 2020	End Jun 2020-Early Jul 2020	8XP
MPW4R0B	45RFSOI_45nm	12/9/2019		12/31/2019	1/13/2020	1/13/2020	1/27/2020	Early Jun 2020-Mid Jun 2020	Early Jun 2020-Mid Jun 2020	45RF
MPW1422	12nm	12/6/2019	10/23/2019	12/7/2019	12/6/2019	12/6/2019	12/11/2019	Mid Mar 2020-End Mar 2020	End Apr 2020-Early May 2020	12LP
MPW9W01	9WG_90nm	11/20/2019		11/21/2019	11/25/2019	11/25/2019	12/9/2019	End Mar 2020-Early Apr 2020	End Mar 2020-Early Apr 2020	9WG
MPW13A8	0.11/0.13um	11/18/2019	11/25/2019	12/17/2019	12/30/2019	12/30/2019	1/13/2020	Mid Mar 2020-Mid Mar 2020	Mid May 2020-Mid May 2020	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD
MPW2232	22nm	11/18/2019	11/25/2019	12/17/2019	12/30/2019	12/30/2019	1/13/2020	Early Mar 2020-End Mar 2020	End Mar 2020-Early Apr 2020	22FDX
MPW0358	28nm	11/11/2019	11/18/2019	12/10/2019	12/23/2019	12/23/2019	1/6/2020	Mid Mar 2020-End Mar 2020	End Mar 2020-Early Apr 2020	28SLP/28SLPe/28SLP-RF/28SLP-HV
								Mid Feb 2020-End	End Feb	22FDX/22eMRAM. Projected ship dates are for

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

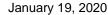
^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



Confidential



MPW2231 | 22nm | 10/17/2019 | 10/24/2019 | 11/14/2019 | 11/28/2019 | 11/28/2019 | 12/12/2019 | Feb 2020 | 2020-Early Mar 2020 | NON-22eMRAM.

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW #	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW0691	55nm	10/7/2019	10/14/2019	11/5/2019	11/18/2019	11/18/2019	12/2/2019	End Feb 2020-Early Mar 2020	End Apr 2020-Early May 2020	55LPX/55LPe/55ULP/55BCDL/55NVM
MPW18F8	0.18um	10/7/2019	10/14/2019	11/5/2019	11/18/2019	11/18/2019	12/2/2019	End Jan 2020-Mid Feb 2020	Mid Mar 2020-Mid Mar 2020	Fab3_IC/BCDlite/UHV/MCU
MPW0461	40nm	9/30/2019	10/7/2019	10/29/2019	11/11/2019	11/11/2019	11/25/2019	Early Mar 2020-Mid Mar 2020	Early May 2020-Mid May 2020	40LP/40HV/40NVM
MPW13A7	0.11/0.13um	9/18/2019	9/23/2019	10/15/2019	10/28/2019	10/28/2019	11/11/2019	Mid Jan 2020-End Jan 2020	Mid Mar 2020-Mid Mar 2020	RFSOI/G/LP/EEPROM/110TS/BCDlite/BCD
MPW4R03	45RFSOI_45nm	9/16/2019		10/15/2019	11/4/2019	11/4/2019	11/18/2019	Early Apr 2020-Mid Apr 2020	End Apr 2020-Early May 2020	45RF
MPW9W00	9WG_90nm	9/13/2019		9/13/2019	9/13/2019	9/13/2019	9/13/2019	Mid Jan 2020-End Jan 2020	Mid Jan 2020-End Jan 2020	9WG
MPW4R0A	45RFSOI_45nm	9/4/2019		9/5/2019	9/4/2019	9/4/2019	9/18/2019	Early Feb 2020-Mid Feb 2020	Early Feb 2020-Mid Feb 2020	45RF
MPW8H01	8HP_0.13um	9/2/2019		10/1/2019	10/14/2019	10/14/2019	10/28/2019	Early Apr 2020-Mid Apr 2020	Early Apr 2020-Mid Apr 2020	8HP_0.13um
MPW0690	55nm	8/19/2019	8/26/2019	9/17/2019	9/30/2019	9/30/2019	10/14/2019	End Dec 2019-Early Jan 2020	End Feb 2020-Early Mar 2020	55LPX/55LPe/55ULP/55BCDL/55NVM
MPW7W00	7WL_0.18um	8/12/2019		9/9/2019	9/23/2019	9/23/2019	10/7/2019	End Feb 2020-Mid Mar 2020	End Feb 2020-Mid Mar 2020	7WL_0.18um

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.



MPW #	Geometry	Reservation Close Date*	CDRS Submit Date [@]	Cancellation Deadline	Dry Run DRC CutOff	Other Participants Tapeout Date ^{#&}	Customer Tapeout Date [#]	Projected 1st Batch Bare Die Ship ^{\$}	Projected 2nd Batch Bare Die Ship ^{\$}	Process Offering
MPW1421	12nm	8/5/2019	8/12/2019	9/3/2019	9/23/2019	9/16/2019	9/30/2019	Mid Dec 2019-End Dec 2019	End Jan 2020-Early Feb 2020	12LP
МРW9H03	9HP_90nm	7/15/2019		8/6/2019	8/19/2019	8/19/2019	9/2/2019	End Feb 2020-Early Mar 2020	End Feb 2020-Early Mar 2020	9НР
MPW0460	40nm	7/8/2019	7/15/2019	8/6/2019	8/19/2019	8/19/2019	9/2/2019	End Nov 2019-Early Dec 2019	Mid Jan 2020-End Jan 2020	40LP/40NVM
MPW8H00	8HP_0.13um	7/2/2019		7/31/2019	8/13/2019	8/13/2019	8/27/2019	Mid Jan 2020-End Jan 2020	Mid Jan 2020-End Jan 2020	8HP_0.13um

^{*:} Quotation Close deadline=Reservation Cancellation deadline=28 days after Reservation Close Date.

^{@:} CDRS must be submitted 1 week after Reservation Cutoff. CDRS will be reviewed with CE & FAB Proto Owner to finalize specific participant requirements and ensure tapeout readiness.

^{#:} Design Rule Checks shall start once participation is confirmed. Designs which still have design rule violations by the Tapeout Date will be rejected from the train.

If Design Rule Checks or IP Merge is needed, please submit requests 2 weeks before the Tapeout Date at the latest.

[&]quot;GDSII Cutoff for DRC" date is applicable only for customer tapeouts. Other Participants have to run DRC on their own and obtain violation waivers (if applicable) before submitting GDSII on Others Participants Tapeout Date.

[&]amp;: Other Participants refers to IP test chips and Foundry internal tapeouts e.g. Technology Department, Reliability Department, etc.

^{\$:} Projected bare die ship is for generic guidance only. Participant assignment to 1st and/or 2nd batch will be based on specific customer requirements, fab utilization & project priority. GLOBALFOUNDRIES reserves the right to change at any time. 1st batch assignment is reserved for POR wafer(no corner split) of Expedited fee paying customers. 2nd batch bare die ship is applicable for corner splits, special processing and standard cycle time lots. The targeted bare die ship dates for specific customer/device will be committed upon lot start by respective MPW CE representatives.